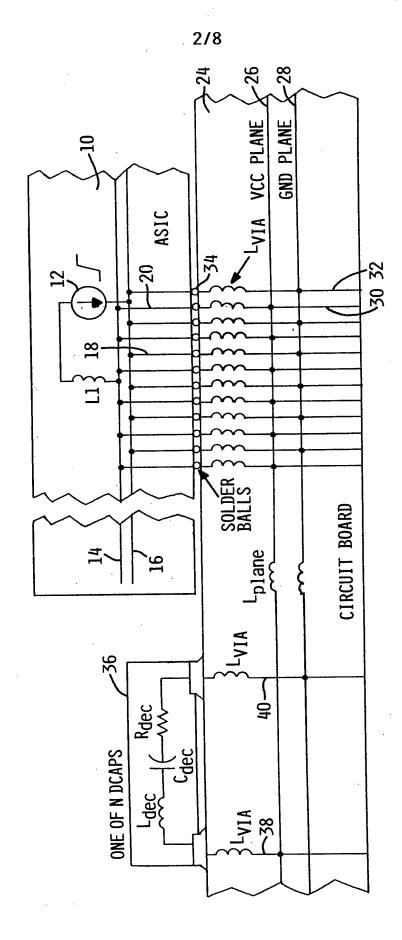
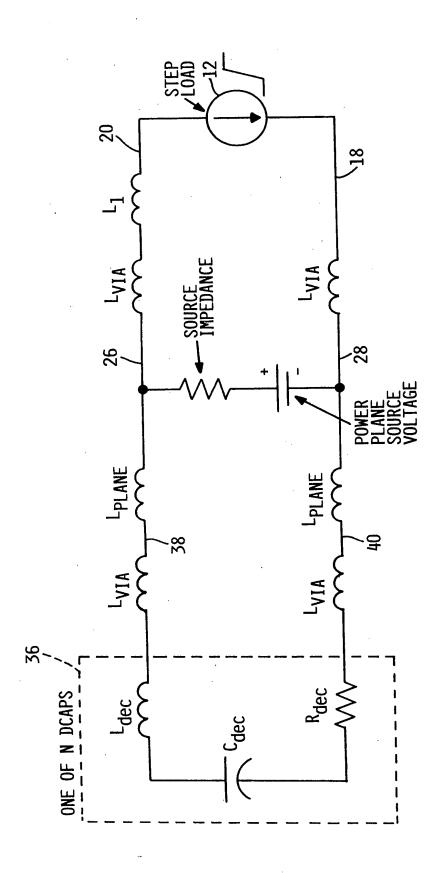


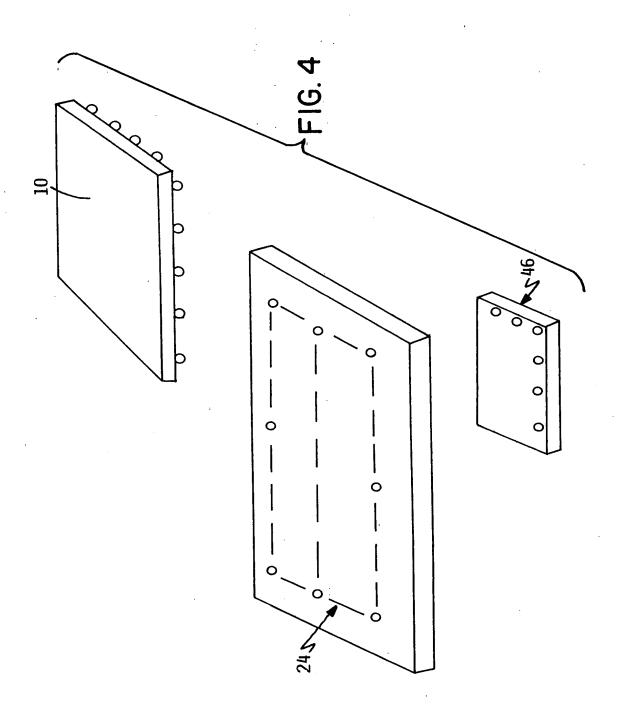
(PRIOR ART) FIG. I



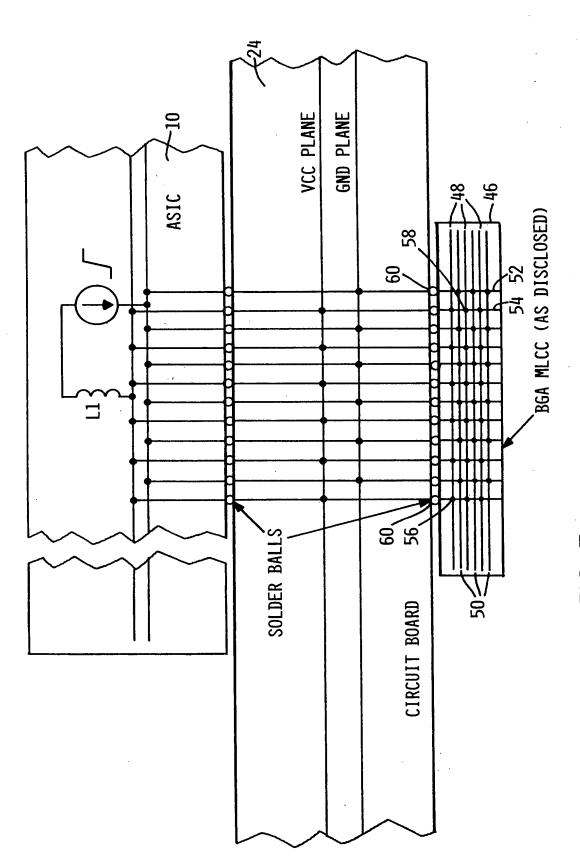
(PRIOR ART) FIG. 2



(PRIOR ART) FIG. 3

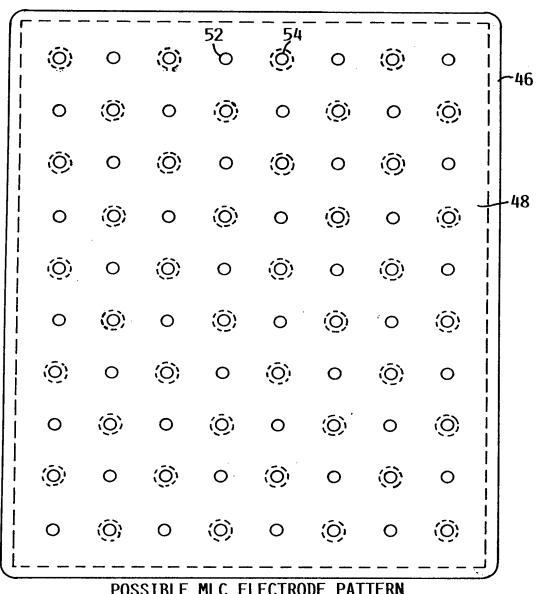






F16.1

+



POSSIBLE MLC ELECTRODE **PATTERN** 

FIG. 6

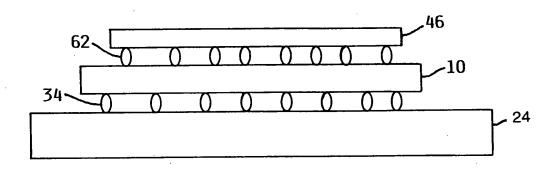


FIG. 7

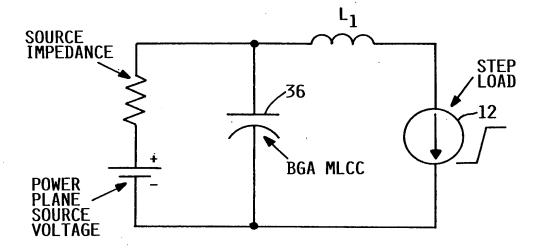


FIG. 8